Docket No.: 50277-1653

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Sashikanth Chandrasekaran, et al.

Confirmation Number: 1436

Group Art Unit: 2187

Serial No.: 09/871,853/Patent No.: 7,107,319

Filed: May 31, 2001/Issued: September 12, 2006 : Examiner: Kimberly N. McClean-Mayo

For:

METHOD AND APPARATUS FOR REDUCING LATENCY AND MESSAGE

TRAFFIC DURING DATA AND LOCK TRANSFER IN A MULTI-NODE

SYSTEM

REQUEST FOR CERTIFICATE OF CORRECTION

Attention Certificate of Corrections Branch Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Enclosed herein is a proposed Certificate of Correction which Applicants respectfully request be entered with respect to the above patent. A copy of the initialed 1449 by the Examiner makes it clear that the omission of the references printed on the patent is the fault of the PTO. Hence, no fee is required,

It is respectfully requested that the Certificate of Correction be issued.

Respectfully submitted.

HICKMAN PALERMO TRUONG & BECKER LLP

/BrianDHickman/ Brian D. Hickman

Registration No. 35,894

Dated: May 31, 2007

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PAGE 05/06

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IN	FORM		DISCLOSURE OF	ATTY. DOCKET 50277-1653	NO.	SERIAL N 09/871,85		
		(P	TO-1449)	APPLICANT: Sashikanth Chandrasekaran, et al.				
				FILING DATE: May 31, 2001			GROUP: 2187	
		11	U.S	. PATENT DOCK	JMENTS			
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		OTHER ART - NO PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No	include name of the author (in CAPITAL LETTERS), title of the arricle (when appropriate), title of the item (book, magazine, journal, strais, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation
KNM		Lory D. Molesky et al., "Database Locking Protocols for Large-Scale Cache- Coherent Shared Memory Multiprocessors: Desigo, Implementation and Performance," June 6, 1995, pp. 1-24	
win		Lory D. Molesky et al., "Efficient Locking for Shared Memory Database Systems," March 1994, 28 pages.	and the second

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

 PATENT NO.
 : 7,107,319

 APPLICATION NO.
 : 09/871,853

 ISSUE DATE
 : September 12, 2006

 INVENTOR(S)
 : Sashikanth Chandrasekaran

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

(56) References Cited

OTHER PUBLICATIONS

Please insert

--Lory D. Molesky et al., "Database Locking Protocols for Large-Scale Cache-Coherent Shared Memory Multiprocessors: Design, Implementation and Performance," June 6, 1995, pages 1-24.

Lory D. Molesky et al., "Efficient Locking for Shared Memory Database Systems," March 1994, 28 pages, --

MAILING ADDRESS OF SENDER (Please do not use customer number below): Hickman Palermo Truong & Becker LLP 2055 Gateway Place, Suite 550 San Jose, CA 95110

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